

BHARAT INSTITUTE OF ENGINEERING AND TECHNOLOGY,(POLYTECHNIC),MOHADA								
5 TH SEMESTER(E.T.C.ENGG.)								
DAY	9.05-9.55	9.55-10.45	10.45-11.35	11.35-12.25	12.25-1.15	1.15-1.55	1.55-2.45	2.45-3.35
MONDAY	ADC	PE	ENTP	VLSI	SCA	L		PE LAB
TUESDAY	ADC	WP & BC ENGG.	PE	SSD	ADC LAB	U		ADC LAB
WEDNESDAY	ENTP	PE	ADC	WP & BC ENGG.	VLSI LAB	N		VLSI LAB
THURSDAY	PPT		ADC	VLSI	WP & BC ENGG LAB	C		WP & BC ENGG. LAB
FRIDAY	PPT		PE	WP & BC ENGG.	VLSI	H		PE LAB
SATURDAY	WP & BC ENGG.	ADC	VLSI	ENTP	SSD	*		PROJECT

THEORY

PRACTICAL

TH-1:- ENTREPRENEURSHIP AND MANAGEMEAENT & SMART TECHNOLOGY(ENTP):-Er.SUCHISMITA GOUDA	A&DC LAB(PR-1):- Er.PURNA CH.NAHAK
TH-2: VERY LARGE SCALE INTEGRATED CIRCUITS (VLSI):- Er.MANASI PADHY	VLSI & EMBEDED SYSTEM(PR-2):- Er.MANASI PADHY
TH-3: ANALOG & DIGITAL COMMUNICATION(A&DC):- Er.PURNA CH.NAHAK	WP&BC ENGG.LAB(PR-3):-Er.SUCHISMITA GOUDA
TH-4: WAVE PROPAGATION & BROADBAND COMMUNICATION ENGG.(WP&BC):-Er.SUCHISMITA GOUDA	PE LAB(PR-4):-
TH-5: POWER ELECTRONICS & PLC(PE):-	PROJECT(PR-5):-Er.LINGRAJ PRADHAN
	SCA:- Er.LINGRAJ PRADHAN

[Signature]
5/10/21
H.O.D

BCC

[Signature]
5/10/21
ACC

[Signature]
5.10.21
Principal
BIET,MOHADA